

REMARKS

Claims 1-17 are all the claims pending in the application. Claims 1-17 presently stand rejected.

Claims 1-3, 5-7, 9-11 and 13-17 are rejected under 35 U.S.C. § 102(b) as being anticipated by Klein et al. (6,138,194).

Claims 4, 8 and 12 are rejected under 35 U.S.C. § 103(a) as being unpatentable over Klein et al. (6,138,194) in view of Watts et al. (6,023,587).

Analysis

Claims 1, 5, 9 and 13 are the only claims in independent form; therefore, the following discussion is initially directed to these independent claims.

One technical feature of the present invention is "detecting whether or not an element which is to be mounted in said data processing apparatus, is mounted in said data processing apparatus while said data processing apparatus is connected to said central processing apparatus". The state change device detects *any internal change* of the bus device (node). The bus device needs to be reset when loading an element (for example, a data recording device such as an optical disc, etc) after ejecting the element. The state change device does not detect whether or not the bus device itself is connected to the bus. However, Klein et al is directed to a state change device that detects whether or not the bus device itself is connected to the bus, rather than the internal changes of the bus device. Therefore the present invention is completely different from Klein et al.

In addition, another technical feature of the claimed invention, according to the independent claims, is "initializing all bus devices which are connected to a bus, when said element is mounted in said data processing apparatus". In the present invention, when detecting whether or not an element is connected to the bus, i.e. an inner element, (for example, a data recording device such as an optical disc, etc of said data processing apparatus), the bus initialization device initializes all bus devices (nodes) which are connected to the bus (serial bus). In other words, all devices which are connected to the bus are reset. When a bus reset occurs, the node that detects the bus reset sends a bus reset signal to all of the nodes connected by the serial bus indicating that a bus reset occurred. However, Klein et al only reset the data processing apparatus when the data processing apparatus is connected to a bus. Therefore, Klein et al does not disclose each and every feature of the present invention.

Moreover the object of Klein et al. is a plug and play implementation method. However, the object of the present invention is free from the polling process for getting time for another processing. In the present invention, settings for each node are performed automatically at the time of connection, and it is possible to connect a new node without having to turn the power off.

In view of the foregoing, claims 1, 5, 9 and 13 are distinguishable from Klein et al.

The remaining rejections are directed to the dependent claims. These claims are patentable for at least the same reasons as the independent claims discussed above, by virtue of their dependency therefrom.

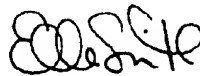
AMENDMENT UNDER 37 C.F.R. § 1.111
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Conclusion

In view of the above, reconsideration and allowance of this application are now believed to be in order, and such actions are hereby solicited. If any points remain in issue which the Examiner feels may be best resolved through a personal or telephone interview, the Examiner is kindly requested to contact the undersigned at the telephone number listed below.

The USPTO is directed and authorized to charge all required fees, except for the Issue Fee and the Publication Fee, to Deposit Account No. 19-4880. Please also credit any overpayments to said Deposit Account.

Respectfully submitted,



Ellen R. Smith
Registration No. 43,042

SUGHRUE MION, PLLC
Telephone: (202) 293-7060
Facsimile: (202) 293-7860

WASHINGTON OFFICE

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